AMENDMENT

Please replace the claims with the following:

- 2. (Unchanged) The method of claim 1, including storing the current video frame in the memory in the computer system.
- 3. (Unchanged) The method of claim 2, wherein the current video frame is written over a previous video frame in the memory.
- 4. (Unchanged) The method of claim 1, wherein computing the difference frame includes computing an exclusive-OR between the current video frame and the previous video frame.

1 5. (Unchanged) The method of claim 1, wherein computing the difference 2 frame includes computing a difference between a block of data from the current 3 video frame and a block of data from the previous video frame. 6. (Unchanged) The method of claim 1, wherein storing the difference 1 2 frame in memory includes storing the difference frame in the memory using block 3 transfers. 1 7. (Unchanged) The method of claim 1, including compressing the video 2 data using the difference frame to produce compressed video data. 1 8. (Unchanged) The method of claim 1, including performing a color space 2 conversion on the video data. 9. (Unchanged) The method of claim 1, including using the video data in 1 2 compressed form in a video teleconferencing system. 10. (Unchanged) The method of claim 1, including storing instructions and 1 2 data for the computer system in the memory.

Claim 11 was previously cancelled.

1 12. (Unchanged) The method of claim 1, wherein computing the 2 difference frame includes computing the difference frame in circuitry outside of a 3 central processing unit in the computer system.

13. (Twice Amended) A method for compressing video data in a computer system, comprising:

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receiving a stream of data from a current video frame in the computer system;

computing a difference frame from the current video frame and a previous video frame as the current video frame streams into the computer system, wherein computing the difference frame includes computing an exclusive-OR between the current video frame and the previous video frame, and wherein computing the difference frame includes computing the difference frame in a core logic chip within the computer system, wherein the core logic chip is a semiconductor chip that couples the processor to a main memory and a system bus for the computer system;

storing the difference frame in a memory in the computer system; storing the current video frame in the memory in the computer system; and compressing the video data using the difference frame to produce compressed video data.

- 1 14. (Unchanged) The method of claim 13, wherein the current video frame 2 is written over a previous video frame in the memory.
- 1 15. (Unchanged) The method of claim 13, wherein computing the 2 difference frame includes computing a difference between a block of data from 3 the current video frame and a block of data from the previous video frame.
- 1 16. (Unchanged) The method of claim 13, wherein storing the difference 2 frame in memory includes storing the difference frame in the memory using block 3 transfers.
 - 17. (Unchanged) The method of claim 13, including using the compressed data in a video teleconferencing system.

- 1 18. (Unchanged) The method of claim 13, including performing a color
- 2 space conversion on the video data.
- 1 19. (Unchanged) The method of claim 13, including storing instructions
- 2 and data for the computer system in the memory.

Claim 20 was previously cancelled.